Amendments to the Specification:

Please replace the paragraph beginning at page 11, line 28 to page 12, line 2, with the following rewritten paragraph:

When a SCSI phase RESELECTION for reconnecting target 172, for example, is indicated on SCSI bus 180, an active signal is driven on line 301 to a clock terminal CLK of register 302. In response to the signal going active on terminal CLK, register 302 drives an active signal on output terminal Q, which in turn results in an active signal (i) on a load terminal LD of reconnecting target address register 305 that in turn loads register 302305, and (ii) on an enable terminal EN of decoder circuit 303 that in turn enables circuit 303.

Please replace the paragraph beginning at page 12, line 3 to line 8, with the following rewritten paragraph:

Thus, the signal going active on line 301 results in the loading of the target ID on SCSI data bus 180A into reconnecting target address register 305 that is also connected to a first input of a comparator 308 by bus 306. Hence, get target operation 201 is performed and the reconnecting target ID is stored in register 305.

Please replace the paragraph beginning at page 12, line 18 to line 28, with the following rewritten paragraph:

The active signal on line 304 is applied to a reset terminal of a counter 352 that is high portion 152 of SCB array pointer 151, and to a load terminal of a register 353 that is low portion 153 of SCB array pointer 151. In response to the rising edge on the load terminal, the tag on SCSI bus 180 is loaded into low

portion ~~152~~153 of SCB array pointer 151, and so operations 202 and 203 are complete. The active signal on line 304 also is applied to a clear terminal of register 302, with a delay if necessary, so that the circuitry is ready for the next target reselection.

Please replace the paragraph beginning at page 13, line 1 to page 13, line 8, with the following rewritten paragraph:

The output signals from register 353 and counter 352 are used to address the SCB at the SCB array site specified by the value in register 353 on page zero 111_0 in extended SCB array 110. The SCSI target address stored in the addressed SCB in page zero 111_0 is a first input signal to comparator 308 via bus 310, and a second input signal to comparator 308 is the reconnecting target address in register 305 via bus 306.

Please replace the paragraph beginning at page 13, line 19 to line 28, with the following rewritten paragraph:

An important aspect of this invention is the reallocation of SCB storage sites in extended SCB array 110 by paged host adapter driver 120, which is stored in memory 122 (Fig. 1), ~~that~~and which executes on CPU 121. As an example, assume that paged host adapter driver 120 has allocated three SCBs with the same tag, e.g., a tag of 8Fh (Fig. 4A), to page zero 111_0 for target one 170, to page one 111_1 for target four 171, and to page two 111_2 for target seven 172. Note that in Fig. 4A the decimal value of the tag, e.g., 143, is used.